**KVM Forum, Brno** 

## Handling <del>Complex Guest</del> MMIO Exits with eBPF

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## \$ whoami

- Upstream kernel hacker
- Arm64 co-maintainer
- Android systems team at Google
- pKVM developer
- Homebrewer
- I'd rather be fishing



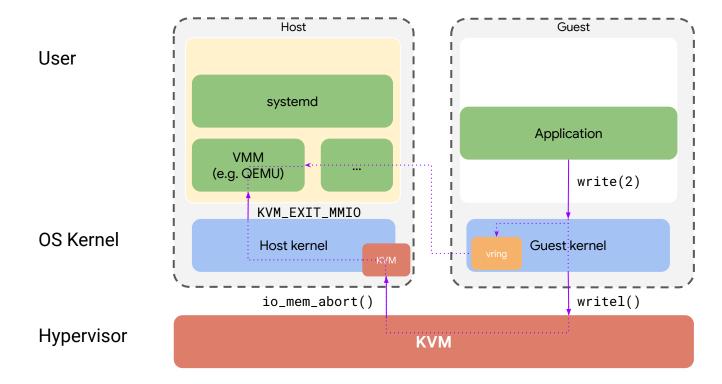
## **Disclaimer!**

- I don't know anything about eBPF
- This is a work-in-progress; eBPF is a moving target
- I'm not convinced it's a sensible idea! Hoping to inspire...
- But it's cool and I fixed a bug
- "Conference-driven development" (I have a prototype)

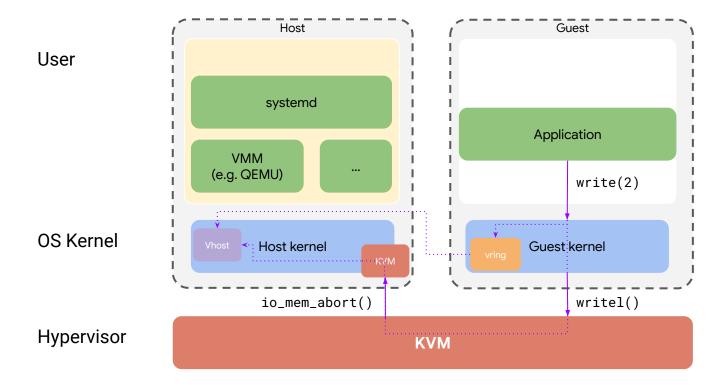




## Basic model for I/O handling in KVM



## Vhost model for I/O handling in KVM



## **Limitations of vhost**

Vhost is widely used to accelerate virtio devices, but it has some limitations:

- Thousands of lines of device-specific C code running in the host kernel
- Only supports virtio; other devices are handled either in userspace or via device-specific KVM\_CREATE\_DEVICE emulation
- The VMM still needs built-in device knowledge to instantiate and manage the in-kernel state
- Hard/impossible to update at runtime
- In-kernel emulation code is privileged and cannot be sandboxed



### "Haha, maybe we should use eBPF to handle guest exits!"

66



android



## "No, seriously."



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## Can eBPF save the day?

#### **Pros:**

- In-kernel sandbox using verifier
- Programs uploaded at runtime
- Flexible/portable ABIs (user and kernel)
- It's fashionable (good for conference submissions ;))

#### Cons:

- Atypical use-case
- Fairly rigid permissions/ACL model
- It's fashionable (moving *very* quickly)

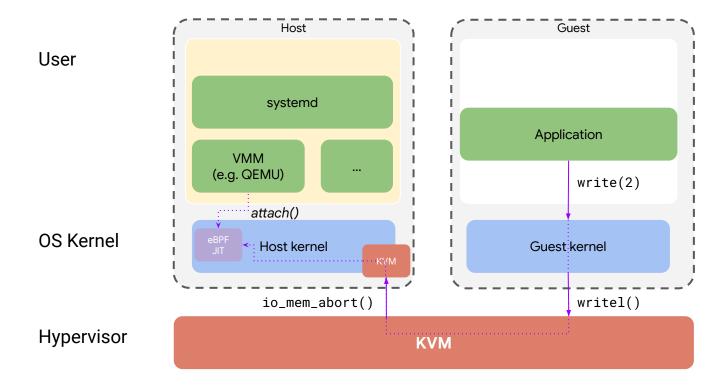


android

# KVM\_DEV\_TYPE\_BPF



## eBPF model for I/O handling in KVM

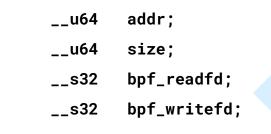


## KVM\_DEV\_TYPE\_BPF: Programming interface

#### Managing the new device type

- Device instantiated via KVM\_CREATE\_DEVICE VM ioctl()
  - KVM\_DEV\_BPF\_ATTR\_GROUP\_REGION attribute to set a new MMIO range and attach bpf progs:

#define KVM\_DEV\_BPF\_ATTR\_GROUP\_REGION 1
struct kvm\_bpf\_user\_region {



File handles returned by bpf(2) BPF\_PROG\_LOAD system call. (libbpf makes this easy)

};

- Envisage a similar approach for vIRQs (eventfds)
  - i.e. Associate eventfds with a region and allow them
    - to be signalled from the eBPF programs



## KVM\_DEV\_TYPE\_BPF: Programming interface

#### View from the eBPF program

- Passed a single context pointer argument by the kernel:
  - o struct bpf\_kvm\_io\_ctx {

\_\_u8 buf[8];

\_\_u64 offset;

\_\_u8 len;

- \_\_u32 :24;
- \_\_u32 vcpu\_id;

};

- Verifier enforces fine-grained permissions on the struct members (e.g. buf is read-only for the MMIO write handler).
- Return value from handler:
  - O: return to guest (skipping faulting instruction)
  - Non-zero: MMIO exit to the VMM

This structure is *fake* and never allocated! JIT generates accesses to the real structures underneath (e.g. the internal vCPU structure)



## BYOD: ELF encapsulation

#### Wrap the device in an ELF file for libbpf

- Implement read/write callbacks in C (or rust)
- eBPF maps for global device state
- ELF note to describe the device configuration such as device-tree compatible string, MMIO size, number of IRQs etc.
- Device.o: ELF 64-bit LSB relocatable, eBPF, version 1 (SYSV), with debug\_info, not stripped
- Different to the usual "skeleton" header approach

Warning: linkers really don't seem to like linking this, so I did terrible things with objcopy 😒

#### .maps

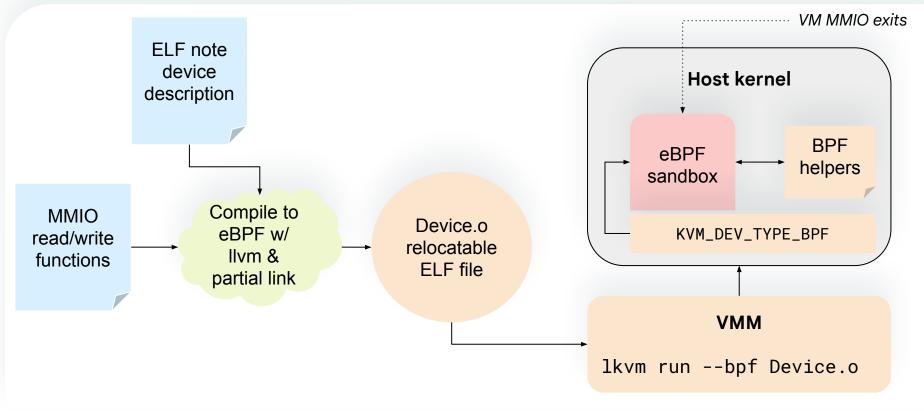
eBPF data structures .note .kvm-bpf .mmio-device

ELF note describing device configuration (e.g. size of MMIO region)

kvm\_io\_read
kvm\_io\_write

eBPF programs to attach to the MMIO callbacks

## Putting it all together



### Live demo

Wish me luck.

ABSOLUTELY NO WARRANTY etc. etc.





Saravana!



## Scheduler hooks (with help)

David!



## Set capacity for guest thread to migrate

3906.7 s +	1.2 s		+18.6 ms	+38.6 ms	+58.6 ms	+78.6 ms	+98.6 ms	+118.6 ms	+138.6 ms	+158.6 ms	+178.6 ms	+198.6 ms	+218.6 ms
\$													
Сри О	* 🗆		thread0-0 [										
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Cpu 0 Frequency	* 🗆	5 GHz	-										
Cpu 6 Frequency	* 🗆	5 GHz											

#### Host - 181ms to Fmax on big CPU.

#### VM - 140ms to Fmax on little CPU. Guest thread never migrates to vCPU1 pinned to big CPU.

1071.3 s +	6.2 s		+18.7 ms	+38.7 ms	+58.7 ms	+78.7 ms	+98.7 ms	+118.7 ms	+138.7 ms	+158.7 ms	+178.7 ms	+198.7 ms	+218.7 ms	+238.7 ms
\$														
Сри О	*	CI	osvm_vcpu0	crosvm_v	vcpu0 [7162]	CLO		crosvm_v	сри0 (7162)				crosvm_vcp	u0 [7162]
Cpu 7	*													
Cpu 0 Frequency	* 2	.5 GHz					_							
Cpu 7 Frequency	* 2	.5 GHz												

#### Source: Saravana's LPC '22 talk: https://lpc.events/event/16/contributions/1195/

### **Problem:**

"Workloads running in a guest VM get terrible task placement and DVFS behavior when compared to running the same workload in the host"

#### https://lore.kernel.org/all/20230330224348.1006691-1-davidai@google.com/

#### **Guest frequency requests Communication channel** Latency Add a new cpufreg driver in the The quest frequency requests need It is *critical* to minimise the latency to reach the host: when processing a guest request: quest: VMM pins the vCPUs New hypercall(s)? Fast-path accesses (e.g. reading current frequency every Guest cpufreg driver advertises MMIO device? context-switch) host CPU properties (e.g. Guess what's coming... Pure overhead: the guest is available frequencies, capacity)

Guest frequency requests result in uclamp utilization requests on the host

- runnable
- State of the system can change

## VCPUFreq device in eBPF

A tiny amount of eBPF code (< 80 lines)!

#### New eBPF helper functions for:

- Querying CPU state:
  - o bpf\_get\_cpu\_freq(cpu)
  - o bpf\_get\_cpu\_max\_hw\_freq(cpu)
  - o bpf\_get\_cpu\_scale(cpu)
- Setting desired uclamp values:
  - bpf\_set\_current\_uclamp(min,max)

These all have corresponding user-accessible interfaces already (sysfs, sched\_setattr()).



Preliminary results in pKVM

### (higher is better)

FIO test	Baseline	Userspace MMIO	eBPF MMIO
Seq write	1.0	1.10	1.15
Rand write	1.0	1.13	1.23
Seq read	1.0	1.03	1.05
Rand read	1.0	1.05	1.09



### I have hacks!

#### Host kernel

git://git.kernel.org/pub/scm/linux/kernel/git/will/linux.git kvm/bpf

- Partial KVM\_DEV\_TYPE\_BPF implementation
  - One memory region per device instance
  - vIRQs not functional yet
  - New program types instead of 'BPF struct\_ops'
- eBPF verifier codegen fix
- Scheduler helpers and minor sched\_setattr() rework

#### Kvmtool

https://android-kvm.googlesource.com/kvmtool willdeacon/bpf

- ELF note parsing and device-tree generation
- Libbpf to extract and load programs
- Instantiation of KVM\_DEV\_TYPE\_BPF device
- Program attachment

#### eBPF devices

#### git://git.kernel.org/pub/scm/linux/kernel/git/will/bpf-devices.git

- Partial PL031 RTC emulation
- vCPUFreq device implementation
- ELF note generation
- Nasty build system hacks to avoid linker crashes
- Completely standalone

#### **Guest kernel**

https://android-review.googlesource.com/c/kernel/common/+/2239182/21

- Guest driver for vCPUFreq device
- Currently per-vCPU register region
  - Banking an alternative?
- AMUs preferred if available

## **Amplify the crazy**



## With great power, comes great... uncertainty?

This all feels quite powerful, but I'm nervous about the ABI and security implications of some of these:

- Asynchronous device behaviour: blocking and signalling?
- bpf\_copy\_from\_user() is bad, but what about bpf guest accessors? To specific windows?
- Vhost as a bpf program
- Finer-grained permissions for BPF programs (a la seccomp?)
- PCI devices (i.e. x86 support)
- Device migration (between VMMs!) using JSON map state
- Guest uploads devices as firmware... (too far?!)

#### • $\Rightarrow$ Your idea here $\Leftarrow$

### Conclusion

I think this is cool but I'm not precious about it.

I'd love it if other folks could have a play and see where they can take it.

The security story needs figuring out properly for some future extensions.



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What next?

## Thank you

